

# Harmonic Analysis of Pwm Based Shunt Compensator Using Dynamic Harmonic Domain

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**Abstract:** -- The extended or dynamic harmonic domain method is used to obtain harmonics during steady dynamic states for static shunt compensation (STATCOM). Firing pulses used for voltage source converter (VSC) are based on pulse width modulation PWM, and the simulation results obtained during steady state are validated by prototype panel experiments. The voltage and current harmonics are observed for STATCOM VSC switching.

## I. INTRODUCTION

Flexible AC transmission techniques based on power electronics devices [1] are used in power systems to solve many power quality issues; however, they also introduce harmonics and power quality problems to these systems. Many firing schemes have been proposed for voltage source converters (VSCs) used as flexible AC transmission systems FACTS devices [2-3] [6]. [9]. [22].[26].[28] Harmonics result in a degradation of system performance. The study of the nonlinear behaviour of power electronics converters has been conducted via various methods including time and harmonic domain [5] [4] [10] [11]. These techniques have been applied to harmonic analysis of thyristor controlled reactors (TCR) [11],[21].The fixed capacitor-thyristor control reactor (FC-TCR ) model in dynamic harmonic domain (DHD) is also available in [17] and the transformer DHD model is given in [19]. The extended harmonic analysis methods provide information from steady state power quality indices to those that are transient. Extended harmonics domain (EHD) is based on an orthogonal series expansion and its properties. The harmonic domain model can be used in extended harmonic domain with few or no modifications. The phase-pulse-width-modulation-based STATCOM are models in the extended harmonic domain [12] [13]. [14]. [18]. [20] [27]. Harmonic coupling can be directly calculated using a harmonic domain model [24]. Some research works have proposed hybrid methods based on time and frequency domain for harmonic analysis [8]. The DHD model of the STATCOM is simulated using selective harmonics elimination methods, or multi-pulse modulation techniques. In this study, the VSC model used as the STATCOM is simulated using a carrier-based PWM method in conjunction with a dynamic or extended harmonics domain method, and the power quality indices including RMS voltage and current are observed for a voltage source converter.DHD simulation results obtained using MATLAB® software are validated using an experimental panel. The results obtained via simulation for a VSC fired by carrier

based PWM techniques are compared with experimental results for power quality analysis.

## II. SWITCHING METHOD FOR VSC

Among various switching schemes available, schemes such as carrier based pulse width modulation for power quality analysis

### Carrier-Based PWM

The generation of a balanced three-phase switching function reduces to a single space vector in either a static or synchronous reference frame. The resultant switching signal, sharing the fundamental period, may be expressed as its Fourier series expansion, and equivalently as a harmonic vector:

$$S_{\psi}(t) = \sum_{k \in \mathbb{Z}} S_{\psi,k} e^{jk\omega_b t} \quad (1)$$

$$s_{\psi}(t) = \Gamma(t) S_{\psi} \quad (2)$$

## III. DYNAMIC HARMONIC DOMAIN MODELLING OF THE STATCOM

A STATCOM circuit is shown in figure 1. It has voltage source converter and capacitor connected to a transmission line through a transformer.

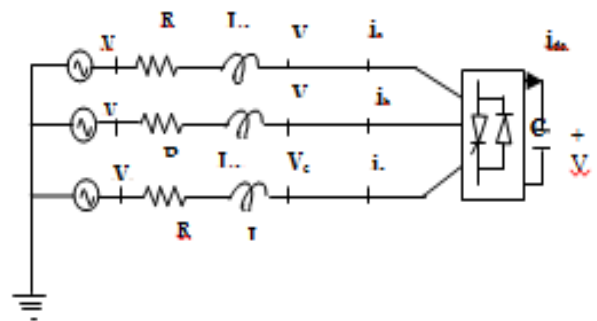


Fig. 1. STATCOM equivalent

**State equation of the STATCOM**

From figure 1, the DC current  $i_{dc}$  expressed in terms of capacitor voltage is given by

$$i_1 = C \frac{dv_{dc}}{dt} \tag{3}$$

The voltage across the impedance in terms of ABC is given by

$$v_{abc} - v_{abc1} = R_{eq} i_{abc}(t) + L_{eq} \frac{di_{abc}(t)}{dt} \tag{4}$$

Here, the  $L_{eq}$  and  $R_{eq}$  are the inductance and resistance of the transformer, respectively, and  $C$  represents the capacitance connected to VSC. Equations 3 and 4 can then be converted into a linear time invariant system equation by means of dynamic harmonic domain as [25]

$$\begin{bmatrix} \dot{I}_{abc}(t) \\ \dot{V}_{dc}(t) \end{bmatrix} = \begin{bmatrix} -\frac{R_{eq}}{L_{eq}} U_1 - D(jH\omega_0) & -\frac{1}{L_{eq}} P_s(t) \\ \frac{1}{C} Q_s(t) & -D(jH\omega_0) \end{bmatrix} \begin{bmatrix} I_{abc}(t) \\ V_{dc}(t) \end{bmatrix} + \frac{1}{L_{eq}} \begin{bmatrix} V_{abc}(t) \\ 0 \end{bmatrix} \tag{5}$$

The power quality indices are obtained by solving (5).

Considering  $\dot{I}_{abc}(t)$  and  $\dot{V}_{dc}(t)$  to be zero in (5) provides the steady state solution.

**IV. SIMULATION OF STATCOM MODEL**

The DHD model of the STATCOM shown in equation (5) is used to examine the dynamic harmonic response for a given condition. The switching used for the voltage source converter is unipolar PWM. The capacitor voltage is assumed to be 0.4 p.u. and is considered to be constant. The reactance and resistance of the coupling transformer are  $L_e=0.2 \times 10^{-3}$  H,  $R_e=4 \times 10^{-2}\Omega$ , respectively, and the capacitance is  $C=4400 \times 10^{-6}$ F. The 50Hz voltages are

$$\begin{aligned} V_{Sa}(t) &= \sin(\omega_0 t) & V_{Ra}(t) &= 0.8 \sin(\omega_0 t) \\ V_{Sb}(t) &= \sin(\omega_0 t - 120^\circ) & V_{Rb}(t) &= 0.8 \sin(\omega_0 t - 120^\circ) \\ V_{Sc}(t) &= \sin(\omega_0 t + 120^\circ) & V_{Rc}(t) &= 0.8 \sin(\omega_0 t + 120^\circ) \end{aligned} \tag{6}$$

At 0.04 s the input voltage  $V_{Ra}(t)$  is changed;

$$\begin{aligned} V_{Ra}(t) &= 0.75 \sin(\omega_0 t) \\ V_{Rb}(t) &= 0.79 \sin(\omega_0 t - 120^\circ) \\ V_{Rc}(t) &= 0.78 \sin(\omega_0 t + 120^\circ) \end{aligned} \tag{7}$$

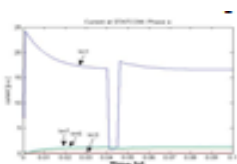


Figure 2a: Current at terminal of STATCOM (Phase A)



Figure 2b: DC side voltage on capacitor

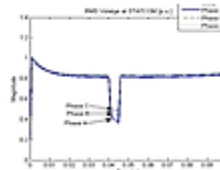


Figure 2c: STATCOM RMS voltage

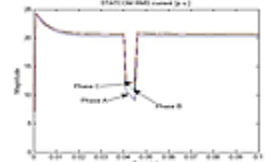


Figure 2d: STATCOM RMS current

Fig.2. : Power quality indices when a unipolar PWM technique is used

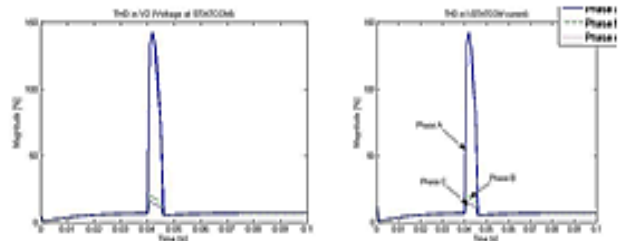


Fig. 3 : Total harmonic distortion in voltage and current

The changes are maintained upto 0.005 s.

A MATLAB code was written for the above model and the program was run using MATLAB software considering function ode(45) and harmonics in order to obtain an accurate result for analysis purposes.

The VSC current, capacitor voltage, root mean square (RMS) current at the voltage source converter, and RMS voltage at the VSC terminals, when using the carrier based switching scheme are shown in Figure 3. Observed total harmonic voltage and current distortion when the carrier based switching firing methods are used are shown in Figures 4.

Figures 2a shows the current, harmonics components such as the third, fifth, and seventh harmonics in the current. The dynamic harmonic response of the capacitor voltage is shown in figure 3b when the carrier based PWM scheme is used. Figures 2b show that the capacitor voltage when the unipolar PWM scheme without using any control scheme. The non-characteristic harmonics are present in the capacitor voltage due to unbalanced nature of the disturbances.

The RMS value of voltage at the STATCOM terminal is shown in figures 2c using a more stable capacitor voltage. The RMS value of current is shown in figures 2d .

Total voltage harmonic distortion at the VSC terminal is shown in figures 3a. Total current harmonic distortion is shown in figures 3b.

**A. Analysis of power quality indices**

The power quality indices shown in table 1 demonstrate that the THD in voltage and current is reduced and capacitor voltage is better utilized during both the steady state and transient state.

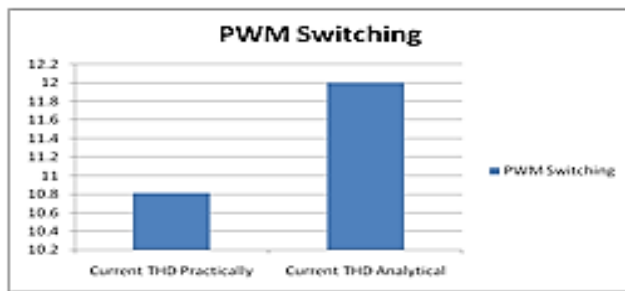
**Table 1: Power quality indices for STATCOM**

	RMS value of Current	THD in current	RMS value of Voltage	THD in voltage
PWM switching	21	12%	0.8	7%

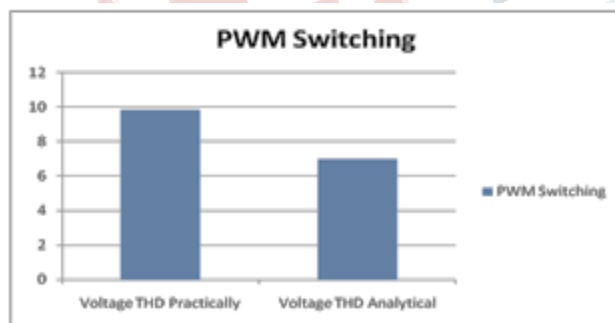
**Table 5: Total harmonic distortion calculation for STATCOM**

**VI. COMPARISON OF ANALYTICAL AND PRACTICAL RESULTS FOR TOTAL HARMONICS DISTORTION IN VOLTAGE AND CURRENT.**

The result obtained using the DHD method is compared with the experimental data for the total harmonic distortion in current and is shown figure 5 for the STATCOM. Similarly, the result obtained via the DHD method is compared with the experimental results for the total harmonic distortion in voltage and is shown in figure 6 for the STATCOM.



**Fig. 5. Comparison of the experimental and analytical results for total harmonic current distortion in current for the STATCOM**



**Fig. 6. Comparison of the experimental and analytical result for total harmonic current distortion for the STATCOM**

The y-axis in figures 5 and 6 show the total percentage harmonics distortion. From figures 5 and 6, it is observed that the analytical and practical results are in agreement with the expected values with a variation of less than 10%. This variation is primarily due to the inductive load and filtering characteristics.

**VII. CONCLUSION**

A STATCOM model has been simulated using PWM switching functions to study the power quality of electrical systems. The results obtained from the simulation of the STATCOM model using the DHD model with PWM techniques in order to obtain power quality indices. The power quality indices obtained by the PWM switching converter switching for VSC-based FACTS devices are compared. This comparison showed the effect of switching on the power quality indices during simulation for a VSC using DHD models, which were validated against the prototype model of transmission line and VSC-based STATCOM controller for the PWM switching using DSP 2407.

**REFERENCES**

- [1] Hingorani, N.G., Gyugyi, L.: 'Understanding FACTS: Concepts and technology of flexible AC transmission systems' (IEEE Press, New York, 2000)
- [2] Ramirez J. S., Medina, A.: 'Modelling of FACTS devices based on SPWM VSC', IEEE Transaction on power Delivery, 2001, 24,(4) pp. 1815-1823
- [3] Saedifard, M., Nikkhajoci, H. and Iravani, R.: 'A space vector modulated STATCOM based on a three-level neutral point clamped converter' IEEE Transaction on power Delivery, 2007, 22,(2) pp. 1029-1039.
- [4] Louie, K. W., Wilson, P., Mazur, R., Kent, K., Dommel, H. W., and Marti, J. R.: 'Power system harmonic analysis in the frequency domain', Canadian Conference on Electrical and Computer Engineering, 2007, pp. 1421-1424.
- [5] Lima, L. T. G., Semlyen, A., M. R. Iravani, M. R.: 'Harmonic domain periodic steady state modeling of power electronics apparatus: SVC and TCSC', IEEE Transactions On Power Delivery, 2003, 18, (3), pp. 960-967
- [6] Nisha, G. K., Ushakumari, S., Lakaparampil, Z. V.: 'Harmonic elimination of space vector modulated three phase inverter', Proceeding International multi-conferences of Engineers and scientists, 2012, 2, March 14-16, 2012, Hong Kong.
- [7] Love, G. N., Wood, A. R.: 'Harmonic state space model of power electronics', The 13th IEEE International Conference On Harmonics And Quality Of Power (ICHQP), September 2008, Wollongong



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- [8] Lian, K. L., Lehn, P. W., 'Steady-state Solution of a voltage-source converter with full closed-loop control', IEEE Transactions On Power Delivery, 2006, 21, (4), pp. 2071-2081
- [9] Srirattanawichaikul, W., Premrudeepreechacharn, S. and Kumsuwan, Y.: 'Modified unipolar carrier-based PWM strategy for three-level neutral-point-clamped voltage source inverters', J Electr Eng Technol, 2014, 9, (2), pp. 489-500
- [10] Yahyaie, F., Lehn, P. W.: 'On dynamic evaluation of harmonics using generalized averaging techniques', IEEE Transactions On Power Systems', 2015, 30, (5), pp. 2216-2224
- [11] Ramirez, A.: 'The modified harmonic domain: interharmonics', IEEE Transactions On Power Delivery, 2011, 26, (1), pp. 235-241
- [12] Rico, J. J., Madrigal, M., Acha, E.: 'Dynamic harmonic evolution using the extended harmonic domain', IEEE Transactions On Power Delivery, 2003, 18, (2), pp. 587-594.
- [13] Vyakaranam, B., Madrigal, M., Villaseca, F. E., Rarick, R.: 'Dynamic harmonic evolution in FACTS via the extended harmonic domain method', Power and Energy Conference at Illinois (PECI), 2010, pp. 29-38
- [14] Zúñiga-Haro, P.: 'Harmonic modeling of multi-pulse SSSC', IEEE Bucharest Power Tech Conference, June 28th-July 2nd, Bucharest, Romania, 2009
- [15] Saedifard, M., Bakhshai, A. R., Joos, G., Jain, P.: 'Modified low switching frequency space vector modulators for high power multi-module converters', Applied Power Electronics Conference and Exposition, 2003. APEC '03. Eighteenth Annual IEEE, pp. 555-561
- [16] Saedifard, M., Nikkhajoei, H., Iravani, R., and Bakhshai, A.: 'A space vector modulation approach for a multimodule HVDC converter system', IEEE Transactions On Power Delivery, 2007, 22, (3), pp. 1643-1654
- [17] García, H., Madrigal, M., Vyakaranam, B., Rarick, R., Villaseca, F. E.: 'Dynamic companion harmonic circuit models for analysis of power systems with embedded power electronics devices', 2011, Electr. Power Syst. Res., 81, (2), pp. 340-346
- [18] Vyakaranam, B., Villaseca, F. E.: 'Dynamic modeling and analysis of generalized power flow controller', Electric Power Systems Research, 2014, 106, pp. 1-11
- [19] García, H., Madrigal, M., Rico, J.J.: 'The use of companion harmonic circuit models for transient analysis and periodic steady state initialization in electrical networks with nonlinearities', Electric Power Systems Research, 2012, 93, pp. 46-53
- [20] Chavez, J. J., Ramirez, A., Naredo, J. L.: 'Dynamic harmonic domain transmission line modeling for transients', Presented at the International Conference on Power Systems Transients (IPST'07) in Lyon, France on June 4-7, 2007.
- [21] Zhijun, E., Chan, K. W., Fang, D. Z.: 'Dynamic phasor modelling of TCR based FACTS devices for high speed power system fast transients simulation', Asian Power Electronics Journal, 2007, 1, (1), pp. 42-48
- [22] Ramya, K., Sri, C. H., Rao, J.: 'Space vector PWM control technique for three phase voltage source converter', International Journal of Advance Research In Science And Engineering, 2014, 3, (10)
- [23] Segundo-Ramírez, J., Bárcenas, E., Medina, A., Cárdenas, V.: 'Steady-state and dynamic state-space model for fast and efficient solution and stability assessment of ASDs', 2011, IEEE Transactions On Industrial Electronics, 58, (7), pp. 2836-2847
- [24] Ormrod, J.E.: 'Harmonic state space modelling of voltage source converters', Ph.D. Dissertation, Univ. Canterbury, New Zealand, 2013.
- [25] Devendra. M Holey, VK Chandrakar, 'Harmonic Domain Modelling of Space Vector Based STATCOM', Energy and power engineering 8 (04), 195
- [26] Vinod K. Chandrakar M V Wankhede, 'Static synchronous compensator (STATCOM) for the improvement of the electrical system performance with non linear load', International journal of power systems 1, 27-32
- [27] DM Holey, VK Chandrakar, 'Harmonic Analysis Techniques of Power System-A Review' International Research Journal of Engineering and Technology (IRJET), Volume: 03 Issue: 02 | Feb-2016
- [28] Pallavi Mahale, Kshiti D Joshi, VK Chandrakar, 'Static synchronous compensator (STATCOM) with Energy Storage', 2nd IEEE International Conference on Emerging Trends in Engineering and Technology, pp 560-563
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